ABSTRACT:

A data carrier, notably a chip card, which includes a data processing unit and at least one contactless interface via which the data processing unit can be coupled to a read/write apparatus in order to exchange data signals and to take up electrical energy for the operation of the data processing unit; the data processing unit is constructed at least mainly while using at least substantially asynchronously operating logic components (asynchronous logic).

The data carrier according to the invention makes optimum use of the energy applied thereto and is at the same time protected against the tapping of the signal processing steps to be executed therein.

Fig. 1